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January 28, 2003

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Honorable Commissioner for Patents
Washington, D.C. 20231
Attention: Examiner K. Wells
Group 2816 Before Final Facsimile No. 703-872-9318

2816
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JAN 28 2003

Re: Miyazaki *et al.*, U.S. Serial No. 09/811,387
Attorney Docket No. 500.39910X00

TECHNOLOGY CENTER 2800

SUBMISSION OF AMENDMENT AND PETITION FOR EXTENSION OF TIME

Sir:

Applicant hereby submits the attached "Amendment" (22 pgs), "Petition for Extension of Time" (1 pg), "Form PTO-2038" (1 pg) in the above-identified application.

CERTIFICATE OF TRANSMISSION:

I hereby certify that the attached "Amendment" (22 pgs), "Petition for Extension of Time" (1 pg), "Form PTO-2038" (1 pg) are being formally filed in the USPTO via Group 2816 BF Facsimile No. 703-872-9318 on January 28, 2003.


Kim Dang

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: MIYAZAKI et al.
Serial No.: 09/811,387 ✓
Filed: March 20, 2001 ✓
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
IN WHICH OPERATING FREQUENCY, SUPPLY
VOLTAGE AND SUBSTRATE BIAS VOLTAGE ARE
CONTROLLABLE TO REDUCE POWER
CONSUMPTION
(As amended)
Group: 2816
Examiner: K. Wells

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TECHNOLOGY CENTER 2800

AMENDMENT

Assistant Commissioner
for Patents
Washington, D.C. 20231

January 28, 2003

Sir:

In response to the Office Action dated August 28, 2002, the period of
response for which extension is requested by the attached Petition for Extension of
Time, please amend the above-identified application as follows:

In the Title:

✓ Please amend the title to read as follows:

- SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE IN WHICH
OPERATING FREQUENCY, SUPPLY VOLTAGE AND SUBSTRATE BIAS
VOLTAGE ARE CONTROLLABLE TO REDUCE POWER CONSUMPTION

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